



#11/68
MS AF
REPLY UNDER
37 C.F.R. § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2815
PATENT
0941-0316P
OK
12/10/03

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Ming-Dou KER et al. Conf.: 9842
Appl. No.: 09/944,171 Group: 2815
Filed: September 4, 2001 Examiner: J.A. FENTY
For: ESD PROTECTION CIRCUIT WITH VERY LOW INPUT
CAPACITANCE FOR HIGH-FREQUENCY I/O PARTS

SUPPLEMENTAL
REPLY AFTER FINAL UNDER 37 C.F.R. § 1.116

MS AF

Commissioner for Patents

December 1, 2003

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

In reply to the Office Action dated July 29, 2003, and supplemental to the Reply filed October 1, 2003, the following remarks are respectfully submitted in connection with the above-identified application.

This reply includes Remarks.

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DEC - 8 2003
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